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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Keiichiro Kata et al.

Serial No.: 08/(not yet assigned)

Filed: September 25, 1995

For: PROCESS FOR MANUFACTURING SEMICONDUCTOR DEVICE  
AND SEMICONDUCTOR WAFER

Commissioner of Patents and Trademarks  
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. 1.56**

Sir:

Under the provisions of 37 C.F.R. 1.97 through 1.99 and pursuant to applicant's duty of disclosure under 37 C.F.R. 1.56, applicant respectfully brings the documents listed on the attached form PTO-1449, and discussed in the Specification, to the attention of the Examiner in charge of the above-identified application. Copies of these documents are provided herewith for the convenience of the Examiner.

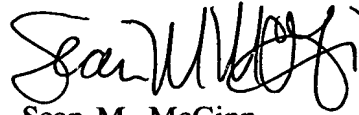
This citation does not constitute an admission that the references are relevant or material to the claims. They are merely cited as constituting related art of which the applicant is aware.

In compliance with the concise explanation requirement under 37 CFR §1.98(a)(3) for foreign language documents, Applicant points out that explanations and concise statements of relevance of the documents are found on pages 3 and 14 of the specification.

WN-1439

It is respectfully requested that these documents be considered by the Examiner and formally made of record in this application.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Sean M. McGinn", with a stylized flourish at the end.

Sean M. McGinn

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